


NOTES:

1. ALL RESISTORS 1% LOW NOISE UNLESS NOTED
2. COMPONENT VALUES THAT ARE NOT SPECIFIED DEPEND UPON STICK SENSITIVITY, GEOMETRY, IMPEDANCE, AND TOLERANCE
3. FOR IMPLEMENTATIONS NOT REQUIRING AUXILIARY MOUSE SUPPORT, DELETE R10 AND R14, AND LEAVE U1 PINS 1 AND 4 UNCONNECTED.
4. 8XC754 RESET PIN MAY BE DRIVEN BY SYSTEM POWER ON RESET SIGNAL (ACTIVE HIGH). OMIT C4, R8 IN THIS CASE.
5. CONNECTION BETWEEN DIGITAL AND ANALOG GROUND MUST BE A SINGLE POINT CONNECTION CLOSE TO THE 8XC754
6. IF JUMPER JMP2 IS INSTALLED, POSITIVE VOLTAGE SWING ON THE STICK'S Y TERMINAL WILL MOVE THE CURSOR DOWNWARD (-Y), OTHERWISE UPWARD.
7. IF JUMPER JMP3 IS INSTALLED, POSITIVE VOLTAGE SWING ON THE STICK'S Z TERMINAL WILL BE INTERPRETED AS DOWNWARD (-Z) FORCE, OTHERWISE UPWARD.

Typical Target Gain Settings

- 35 counts per Z DAC step
- 18 (high order) counts per XY DAC step
- 3.2 grams per (high order) count XY
- 10 grams per count Z

Schematic valid for firmware versions YKT3B, 3D, 3E

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TrackPoint IV Reference Schematic	
Designer Bob Olyha	Revision D
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