



STICK WIRING MAY VARY
TYPICAL WIRING SHOWN FOR REFERENCE ONLY

NOTES:

1. ALL RESISTORS 1% LOW NOISE UNLESS NOTED
2. MIDDLE BUTTON SWITCH IS OPTIONAL
3. JUMPER JMP1 IS ONLY INSTALLED WHEN MIDDLE BUTTON IS NOT PRESENT. OTHERWISE IT IS NOT POPULATED.
4. CONNECTORS ARE SHOWN FOR REFERENCE ONLY
5. CONNECTION BETWEEN ANALOG AND DIGITAL GROUND MUST BE A SINGLE POINT CONNECTION CLOSE TO THE SXC754
6. COMPONENT VALUES THAT ARE NOT SPECIFIED DEPEND UPON STICK SENSITIVITY, GEOMETRY, AND TOLERANCE
7. SXC754 RESET PIN (PIN 5) CAN BE DRIVEN BY SYSTEM POWER ON RESET SIGNAL (ACTIVE HIGH). OMIT C4, R8 IN THIS CASE.
8. FOR IMPLEMENTATIONS NOT REQUIRING AUXILIARY MOUSE SUPPORT, DELETE R10 AND R14, AND LEAVE U1 PINS 4 AND 23 UNCONNECTED.
9. A Z AXIS SERIES RESISTOR AND A FIFTH STICK TERMINAL MAY BE PRESENT.
10. CIRCUIT MUST BE POWERED FROM AN EXTERNAL +5V DC SOURCE, WITH THE CONNECTIONS TO GROUND AND +5V MADE AT A SINGLE POINT.
11. A FALLING EDGE ON RTS WILL GENERATE AN ID STRING
12. A HI LEVEL ON DTR WILL INHIBIT TRANSMISSIONS
13. IF JUMPER JMP2 IS INSTALLED, POSITIVE VOLTAGE SWING ON THE STICK'S Y TERMINAL WILL MOVE THE CURSOR DOWNWARD (-Y), OTHERWISE UPWARD AND POSITIVE VOLTAGE SWING ON THE STICK'S Z TERMINAL WILL BE INTERPRETED AS DOWNWARD (-Z) FORCE, OTHERWISE UPWARD. INDEPENDENT AXIS INVERSIONS MUST BE CONTROLLED VIA SOFTWARE.

Typical Target Settings

50 counts per Z DAC step
18 counts per XY DAC step
3.2 grams/count XY
10 grams/count Z

FIRMWARE VERSION YKT3C

SCHEMATIC: C:\F7362\PHILIPS\SER754.P01

